



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Blossfeld et al. GROUP: 2862
SERIAL NO: 09/743,926 EXAMINER: Jay M. Patidar
FILED: March 19, 2001
FOR: INTEGRATED CIRCUIT WITH A SENSOR ELEMENT FOR
PROVIDING AN ENCODED OUTPUT SIGNAL

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Official Draftsman

TRANSMITTAL OF FORMAL DRAWINGS

Applicant submits herewith new drawing(s) for this application. Attached please find (3) sheets of formal drawings for this application.

The three month period of response set in the Notice of Allowability (PTOL 37) expires on November 11, 2004 and this submission is on or before this expiry date.

Respectfully submitted,

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, Attn: Official Draftsman.

Name: TBellamy

Date: 11-5-04